Please find below and/or attached an Office communication concerning this application or proceeding.

Commissioner of Patents and Trademarks

☐ Responsive to communication(s) filed on _____

☐ This action is **FINAL.**

☐ Since this application is in condition for allowance except for formal matters, **prosecution as to the merits is closed** in accordance with the practice under *Ex parte Quayle*,1935 C.D. 11; 453 O.G. 213.

A shortened statutory period for response to this action is set to expire _____3____ month(s), or thirty days, whichever is longer, from the mailing date of this communication. Failure to respond within the period for response will cause the application to become abandoned. (35 U.S.C. § 133). Extensions of time may be obtained under the provisions of 37 CFR 1.136(a).

Disposition of Claim

☒ Claim(s) *1-20* _____ is/are pending in the applicat

    Of the above, claim(s) _____ is/are withdrawn from consideration

☒ Claim(s) *2 and 6-9* _____ is/are allowed.

☒ Claim(s) *1, 3-5, and 10-20* _____ is/are rejected.

☐ Claim(s) _____ is/are objected to.

☐ Claims _____ are subject to restriction or election requirement.

Application Papers

☒ See the attached Notice of Draftsperson's Patent Drawing Review, PTO-948.

☐ The drawing(s) filed on _____ is/are objected to by the Examiner.

☐ The proposed drawing correction, filed on _____ is ☐ approved ☐ disapproved.

☐ The specification is objected to by the Examiner.

☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. § 119

☐ Acknowledgement is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d).

    ☐ All ☐ Some* ☒ None of the CERTIFIED copies of the priority documents have been

        ☐ received.

        ☐ received in Application No. (Series Code/Serial Number) _____ .

        ☐ received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

    *Certified copies not received: _____

☐ Acknowledgement is made of a claim for domestic priority under 35 U.S.C. § 119(e).

Attachment(s)

☒ Notice of References Cited, PTO-892

☒ Information Disclosure Statement(s), PTO-1449, Paper No(s). _____6_____

☐ Interview Summary, PTO-413

☒ Notice of Draftsperson's Patent Drawing Review, PTO-948

☐ Notice of Informal Patent Application, PTO-152

— SEE OFFICE ACTION ON THE FOLLOWING PAGES —

DETAILED ACTION

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

2. Claims 1, 3-5 and 10-20 are rejected under 35 U.S.C. 102(b) as being by McNally (US Pat. No. 5,608,418).

3. In regard to claims 1, 3-5 and 10-20, McNally discloses a method, comprising: the computer graphics system (10) comprises a processor (20), a graphic subsystem (22), a display (26), and a CRT display (28). McNally teaches that while power is being supplied to the processor (20) communicates with the graphics subsystem (22) over a system bus (24). The processor (20) executes computer graphics application programs. The computer graphics application program generate graphics data that define graphical elements for display. The processor (20) processing data including the varying visual information (Fig. 1, col. 3, lines 1-15). McNally also disclose the graphics subsystem (22) comprises a rendering processor (40), a RAMDAC (42), a color buffer circuit (44), a power circuit (54) and a video random access memory (46), a bus connector (60) couples the system bus (24) for communication to the rendering processor (40), the color buffer circuit (44), a programmable clock generator circuit (50) and (ID-PROM) 52 over a data bus (70). The processor (20) reads the sense data bits from the internal register of the color buffer circuit (44) over the data bus (70) if the CRT sense signals (84) indicate that the CRT displaying (28) is not coupled to the CRT interface lines (32). The

processor (20) uses the sense data bits to determine a configuration for graphics subsystem (22)

to drive the flat panel display (26). The processor (20) accordingly programs the PCG (50), the

rendering processor (40) and the RAMDAC (42) to drive the flat panel display (26) which all

function as claimed (Figs 2 and 3, cols 3 and 4).

Allowable Subject Matter

4.   Claims 2 and 6-9 are allowed.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner

should be directed to Ali Zamani whose telephone number is (703) 308-6414. The examiner can

normally be reached on Monday through Friday from 8:00 a.m to 5:00 p.m.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor,

Richard A. Hjerpe, can be reached on (703) 305-4709. The fax phone number for the

organization where this application or proceeding is assigned is (703) 308-9051.

Any inquiry of a general nature or relating to the status of this application or proceeding

should be directed to the receptionist whose telephone number is (703) 305-3900.

Ali Zamani

November 3, 2000

RICHARD HJERPE
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600